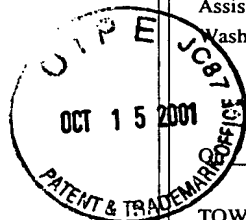


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PATENT  
Attorney Docket No.: 019546-020110US

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Washington, D.C. 20231



*October 2, 2001*

TOWNSEND and TOWNSEND and CREW LLP

By:

*June Curran Can*

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

THOMAS A. KEAN

Application No.: 09/747,759

Filed: December 21, 2000

For: METHOD AND APPARATUS  
FOR SECURE  
CONFIGURATION OF A FIELD  
PROGRAMMABLE GATE  
ARRAY

Examiner: Unassigned

Art Unit: 2818

SUPPLEMENTAL INFORMATION  
DISCLOSURE STATEMENT  
UNDER 37 CFR §1.97 and §1.98

Assistant Commissioner for Patents  
Washington, D.C. 20231

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Sir:

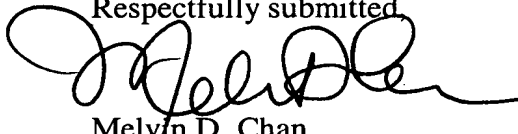
The references cited on attached form PTO-1449 are being called to the attention of the Examiner. Copies of the references are enclosed. It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

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Respectfully submitted



Melvin D. Chan  
Reg. No. 39,626

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Two Embarcadero Center, 8<sup>th</sup> Floor  
San Francisco, California 94111-3834  
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MDC/acc

671 2818

PTO/SB/21 (08-00)

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**TRANSMITTAL  
FORM**

(to be used for all correspondence after initial filing)

Application Number 09/747,759

Filing Date December 21, 2000

First Named Inventor Kean, Thomas A.

Group Art Unit 2818

Examiner Name Unassigned

Total Number of Pages in This Submission

7+refs

Attorney Docket Number 019546-020110

**ENCLOSURES (check all that apply)**☐ Fee Transmittal Form☐ Fee Attached☐ Amendment / Response☐ After Final☐ Affidavits/declaration(s)☐ Extension of Time Request☐ Express Abandonment Request☒ Information Disclosure Statement☐ Certified Copy of Priority Document(s)☐ Response to Missing Parts/  
Incomplete Application☐ Response to Missing  
Parts under 37 CFR  
1.52 or 1.53☐ Assignment Papers  
(for an Application)☐ Drawing(s)☐ Licensing-related Papers☐ Petition Routing Slip (PTO/SB/69)  
and Accompanying Petition☐ Petition to Convert to a  
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Change of Correspondence Address☐ Terminal Disclaimer☐ Request for Refund☐ CD, Number of CD(s)☐ After Allowance Communication to  
Group☐ Appeal Communication to Board of  
Appeals and Interferences☐ Appeal Communication to Group  
(Appeal Notice, Brief, Reply Brief)☐ Proprietary Information☐ Status Letter☒ Other Enclosure(s)  
(please identify below):

1) PTO/SB/08A (3 pages)

2) Photocopies of 42/43 references  
cited on PTO/SB/08A3) Entire 758 page book  
entitled Applied Cryptography  
(reference number 28)

4) Return Receipt Postcard

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**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**Firm and Individual name Townsend and Townsend and Crew LLP  
Melvin D. Chan

Reg No. 39,626

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Sheet	3	of	3
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**Complete if Known**

<b>Application Number</b>	09/747,759
<b>Filing Date</b>	Dec. 21, 2000
<b>First Named Inventor</b>	Kean, Thomas
<b>Group Art Unit</b>	2818
<b>Examiner Name</b>	Unassigned
<b>Attorney Docket Number</b>	019546-020110US

[illegible]

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[illegible]

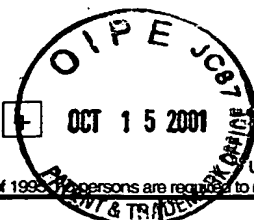
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**Complete If Known**

Application Number	09/747,759
Filing Date	Dec. 21, 2000
First Named Inventor	Kean
Group Art Unit	2818
Examiner Name	Unassigned
Attorney Docket Number	019546-020110US

Sheet 1 of 3

## **U.S. PATENT DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
	01	4,120,030		Johnstone	10-10-1978	
	02	4,465,901		Best	08-14-1984	
	03	4,525,599		Curran	07-25-1985	
	04	4,562,305		Gaffney	12-31-1985	
	05	4,603,381		Guttag	07-29-1986	
	06	4,633,388		Chiu	12-30-1986	
	07	4,847,902		Hampson	07-11-1989	
	08	5,224,166		Hartmann, Jr.	06-29-1993	
	09	5,307,318		Nemoto	04-26-1994	
	10	5,349,249		Chiang	09-20-1994	
	11	5,386,469		Yearsley	01-31-1995	
	12	5,388,157		Austin	02-07-1995	
	13	5,768,372		Sung	06-16-1998	
	14	5,773,993		Trimberger	06-30-1998	
	15	5,835,402		Rao	11-10-1998	
	16	5,898,776		Apland	04-27-1999	
	17	5,915,017		Sung	06-22-1999	
	18	5,963,104		Buer	10-05-1999	
	19	5,970,142		Erickson	10-19-1999	
	20	5,991,880		Curd	11-23-1999	

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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
	21	WO	98/58305	A1	Deas	12-23-1998		<input type="checkbox"/>
	22	WO	00/49717	A2	Trimberger	08-24-2000		<input type="checkbox"/>
	23	WO	99/46774	A1	Kelem	09-16-1999		<input type="checkbox"/>
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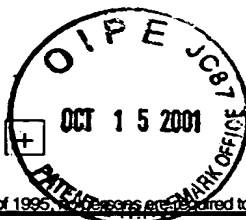
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Sheet 2 of 3

## **Complete If Known**

Application Number	09/747,759
Filing Date	Dec. 21, 2000
First Named Inventor	Kean, Thomas
Group Art Unit	2818
Examiner Name	Unassigned
Attorney Docket Number	019546-020110US

## **OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	24	XILINX INC., "Xilinx User Guide and Tutorials", 1991, Section "The Best of Xcell", p602, column headed "Design Security"	<input type="checkbox"/>
	25	XILINX INC., "XACT Reference Guide", 1993, Section 5 "Peripherals and Hardware", p602, subsection headed "Programming the XC3720"	<input type="checkbox"/>
	26	JOHN G. PROAKIS, "Digital Communications", 3rd edition, McGraw Hill, 1995, Chapter 8. pp413-528	<input type="checkbox"/>
	27	JOHN V. OLDFIELD and RICHARD C. DORF, "Field Programmable Gate Arrays", Wiley-Interscience, 1995, Chapter 3, pp53-102	<input type="checkbox"/>
	28	BRUCE SCHNEIER, "Applied Cryptography", 2nd Edition. John-Wiley, 1996	<input type="checkbox"/>
	29	ACTEL CORP., "Protecting your Intellectual Property from the Pirates", presentation at DesignCon '98, 1998	<input type="checkbox"/>
	30	ATMEL CORP., application note "Programming Specification for Atmel's AT17 and AT17A series FPGA configuration EEPROMs", 1999	<input type="checkbox"/>
	31	TRISCEND INC., "Triscend E5 Configurable Processor Family", Product Description (Preview), July 1999. Section "System Initialisation" p39-43	<input type="checkbox"/>
	32	ACTEL CORP., "60RS Family SPGA's", Advanced Data Sheet, Nov. 19, 1999.	<input type="checkbox"/>
	33	XILINX INC., "Virtex 2.5V Field Programmable Gate Arrays," Advanced Product Specification, 1999, pp 1-22	<input type="checkbox"/>
	34	TRISCEND INC., "Implementing Secure Remote Updates using Triscend E5 Configurable System-on-Chip Devices", Application Note AN-02, January 2000	<input type="checkbox"/>
	35	INTEL CORP., "Intel Processor Identification and the CPUID Instruction", Application Note AP-485, Intel Corporation, Nov. 2000.	<input type="checkbox"/>
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